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Fig. 1

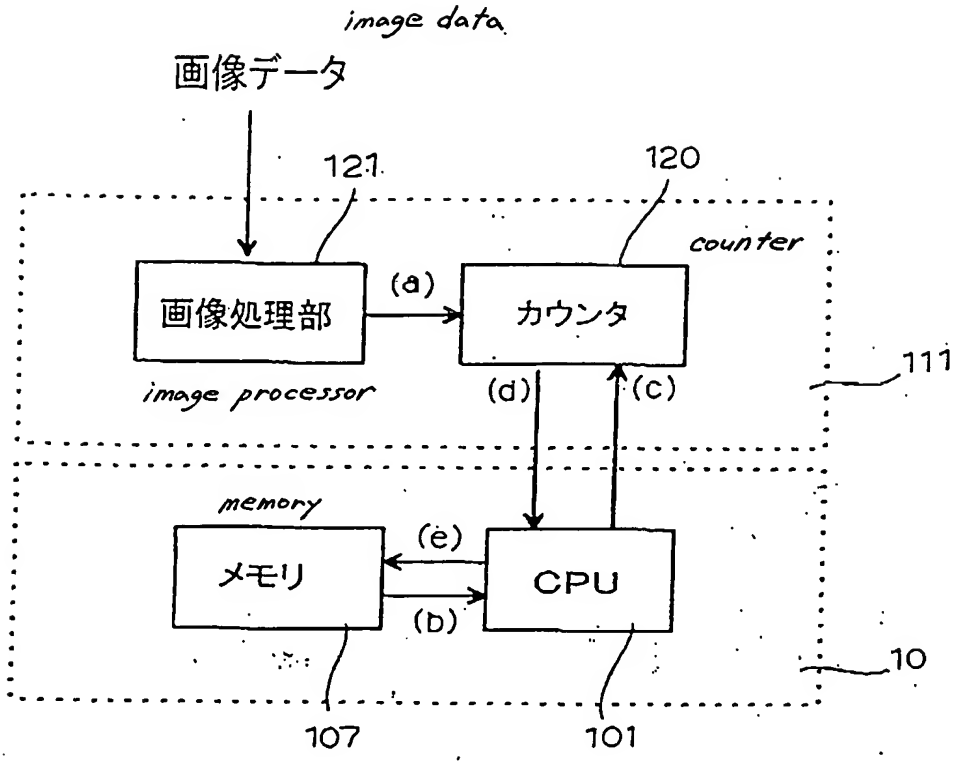


image data
画像データ
121
120
counter
画像処理部
(a)
カウンタ
111
image processor
(d)
(c)
memory
(e)
メモリ
CPU
(b)
10
107
101

Fig. 2

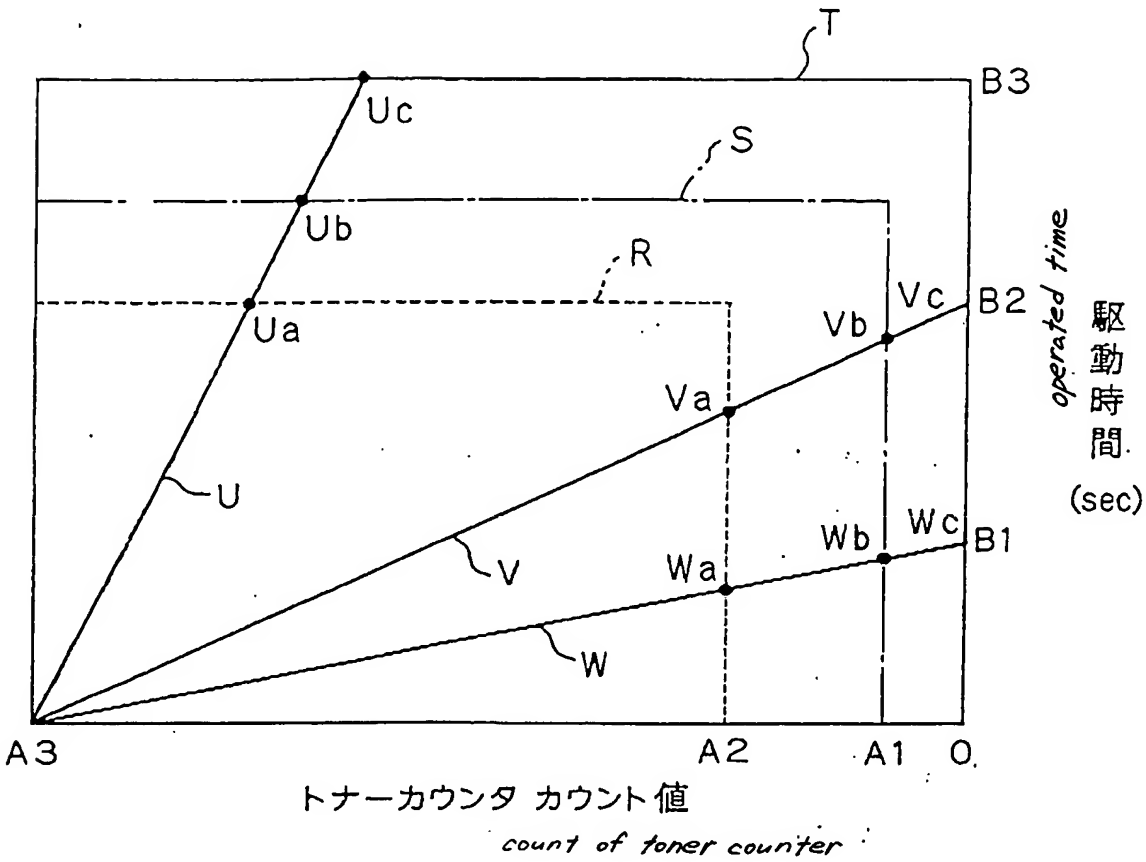

T
B3
Uc
S
Ub
R
Ua
Vc
B2
Vb
Va
U
V
Wb
Wc
B1
Wa
W
A3
A2
A1
0
operated time
駆動時間
(sec)
トナーカウンタ カウント値
count of toner counter

Fig. 3

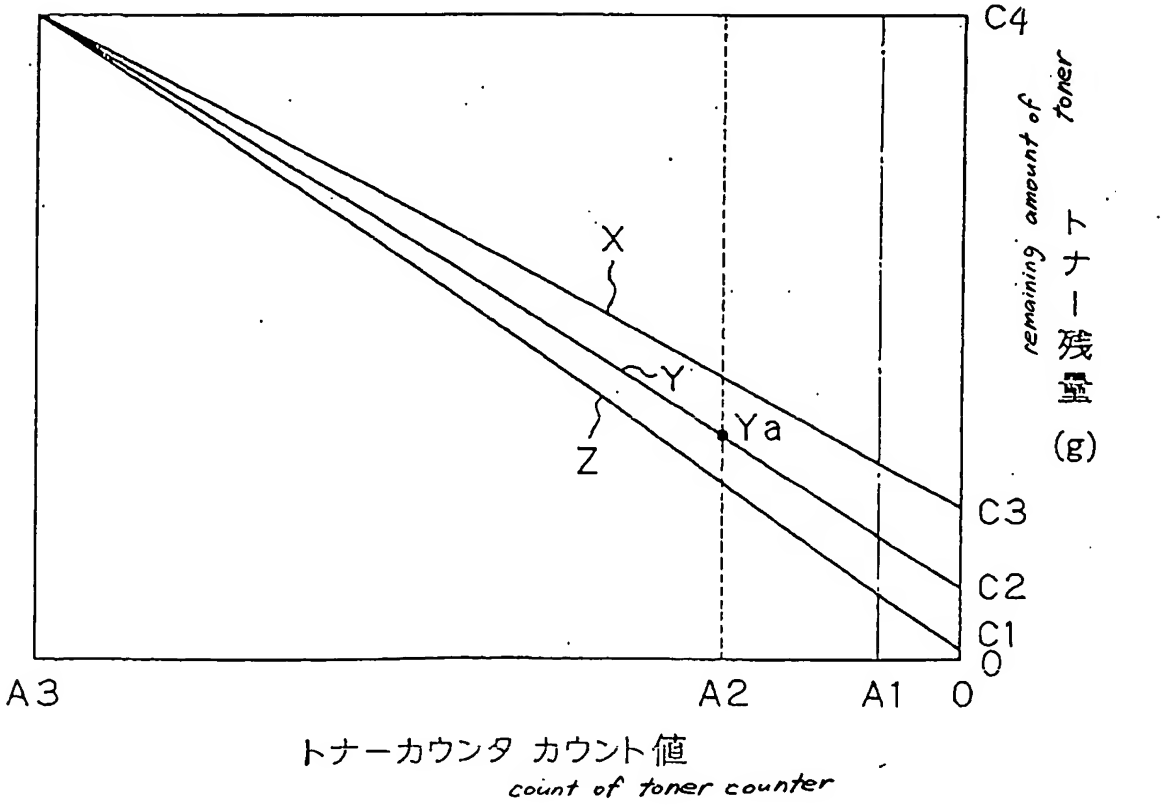

C4
X
Y
Ya
Z
C3
C2
C1
0
A3
A2
A1
0
remaining amount of toner
トナー残量
(g)
トナーカウンタ カウント値
count of toner counter

Fig. 4

color	offset value
色	オフセット値
K	a 1
C	a 2
M	a 3
Y	a 4

Fig. 5

color	offset value
色	オフセット値
K	b 1
C	b 2
M	b 3
Y	b 4

offset value

color main controller engine controller

色	オフセット値	
	メインコントローラ	エンジンコントローラ
K	c1	d1
C	c2	d2
M	c3	d3
Y	c4	d4

Fig. 6

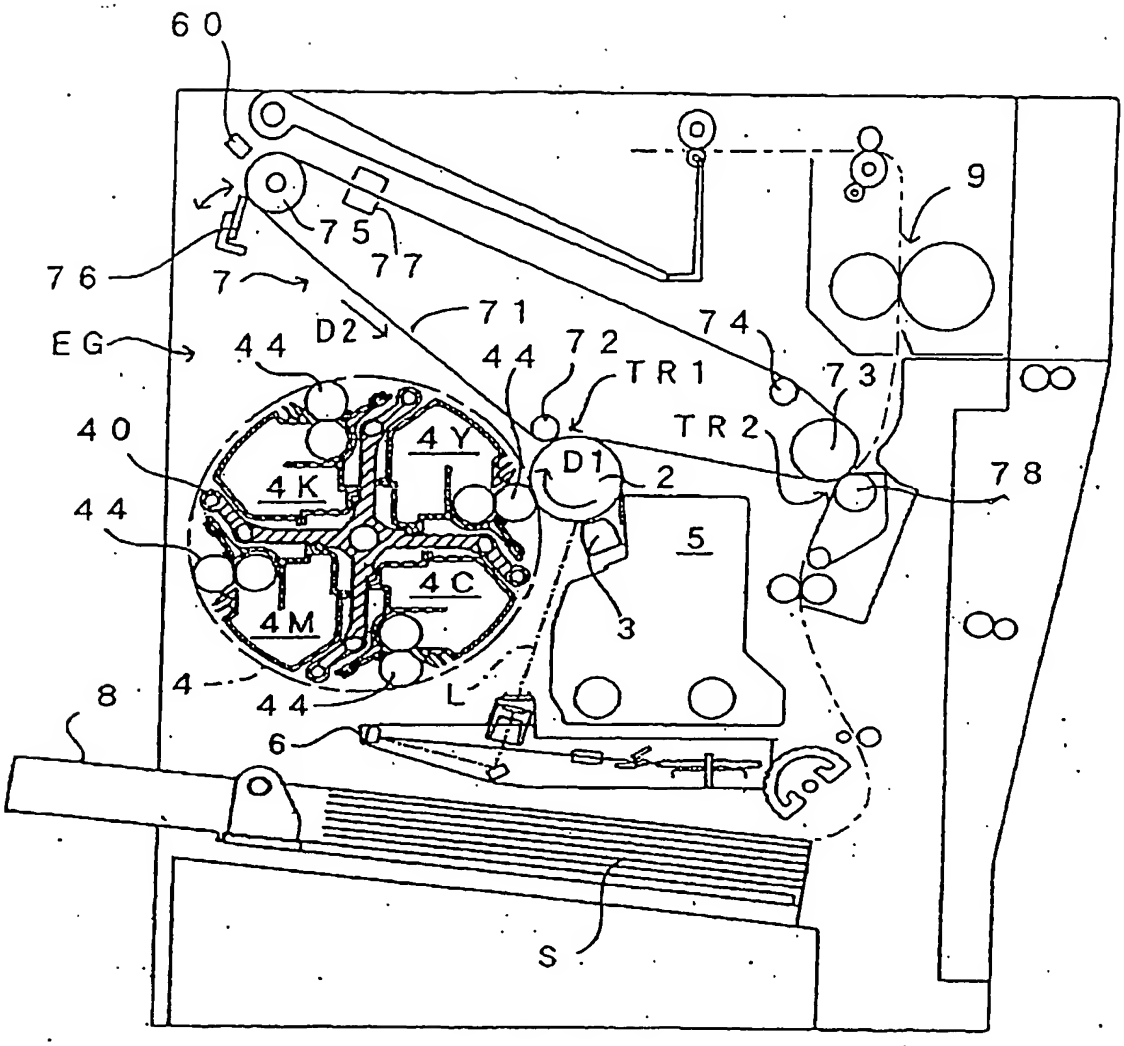

60
75
77
9
76
7
71
74
EG
44
D2
44
72
TR1
73
40
4Y
TR2
4K
D1
2
78
44
5
4C
3
4M
4
44
L
8
6
S

Fig. 7

Fig. 8

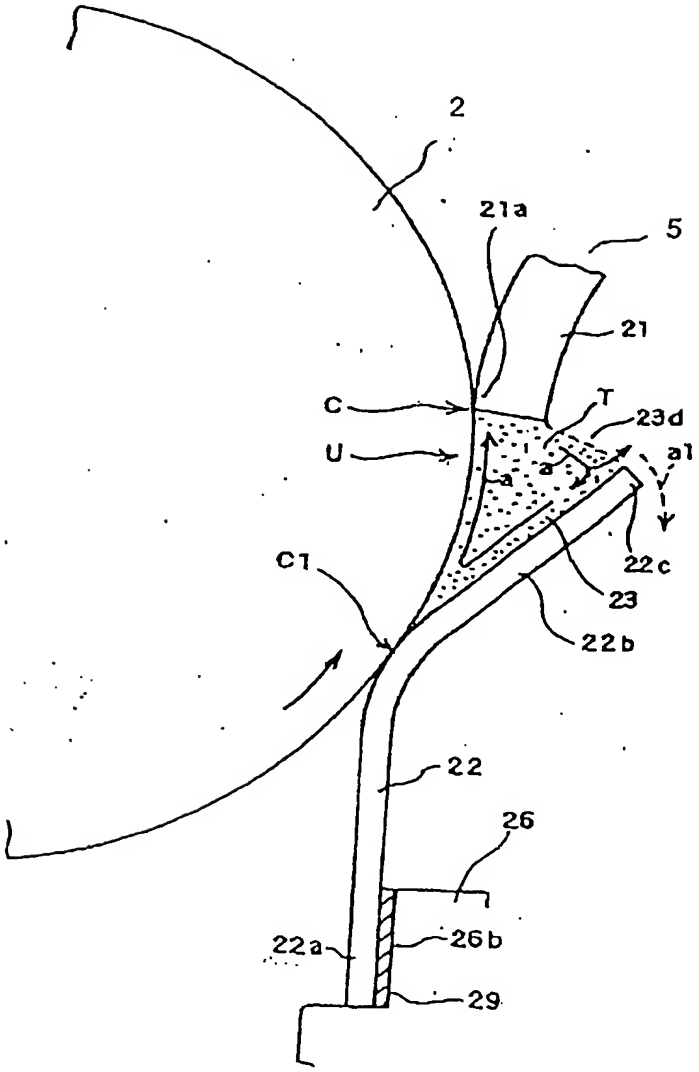
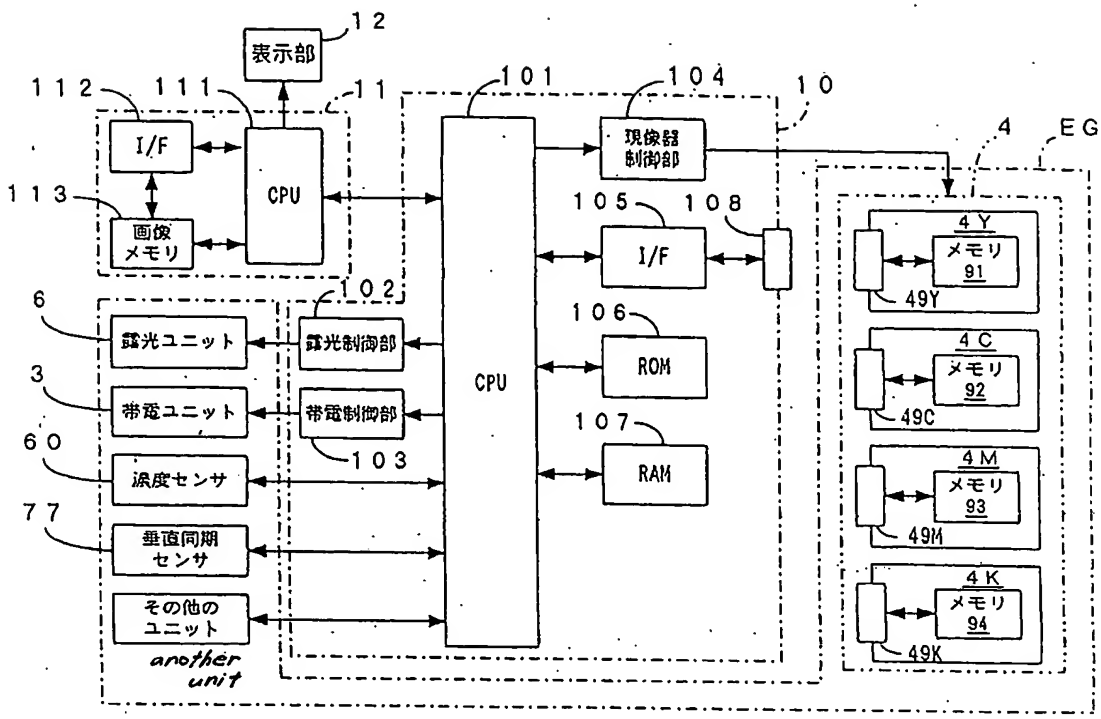


Fig. 9



Hidenori KIN Q80711
DEVICE AND METHOD OF CALCULATING
Darryl Mexic 202-293-7060
March 26, 2004
7 of 9

3: charger

6: exposer

12: display

49Y, 49C, 49M, 49K: connector

60: temperature sensor

77: vertical synchronization sensor

102: exposure controller

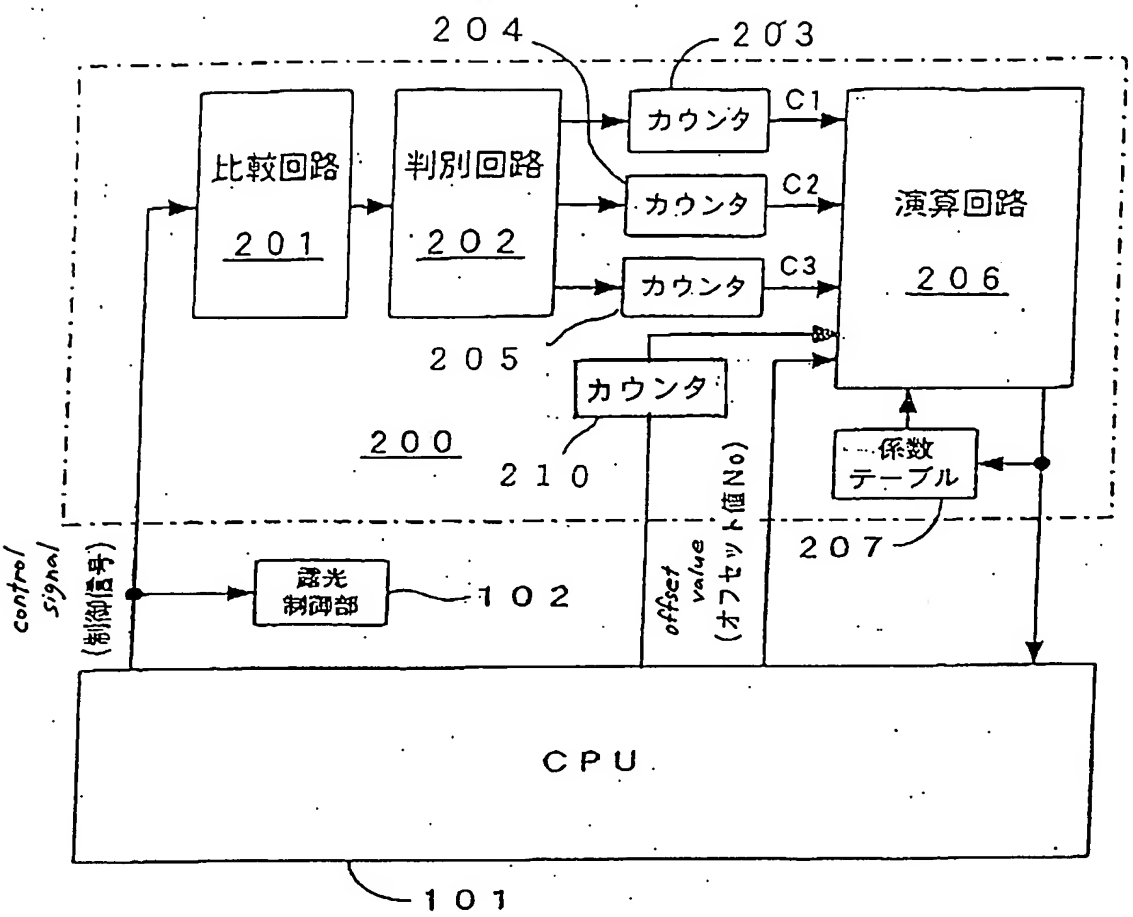
103: charging controller

104: development controller

108: connector

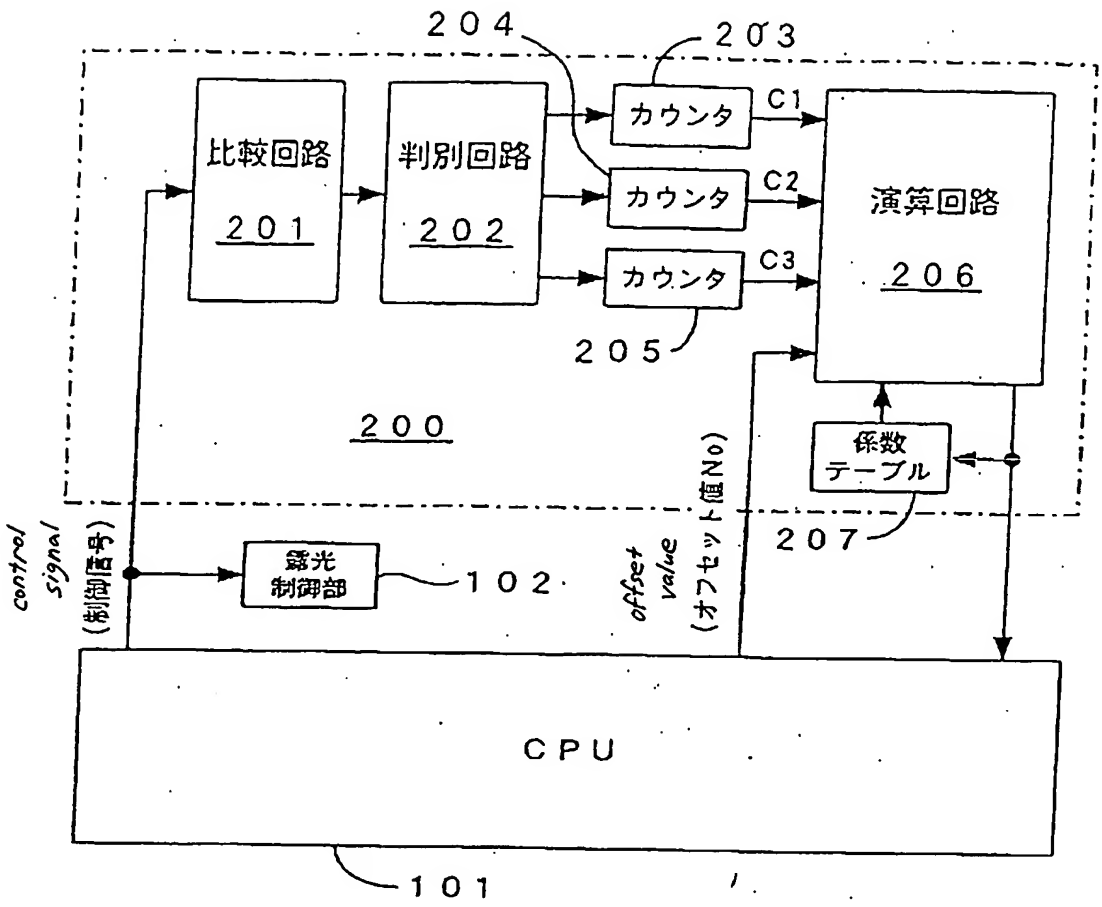
113: image memory

Fig. 10



102: exposure controller
201: comparator
202: discriminator
203, 204, 205: counter
206: calculator
207: coefficient table
210: counter

Fig. 11



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DEVICE AND METHOD OF CALCULATING
Darryl Mexic 202-293-7060
March 26, 2004
9 of 9

102: exposure controller
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